

# DSL

<b>DSL modem chip</b>	Lantiq
<b>DSL modem chip name</b>	Lantiq-VRX500
<b>DSL modem chip serial number</b>	SPPRO1
<b>DSL modem chip hardware revision</b>	VRX-DFE_V1.2
<b>DSL operating-mode</b>	-
<b>Path mode</b>	-
<b>State</b>	UP
<b>Training results</b>	NONE
<b>Mode LO / I2</b>	-
<b>VPI/VCI</b>	-

## Upstream / Downstream (each separately)

	<b>Upstream</b>	<b>Downstream</b>
<b>Actual Data Rate</b>	1.461	5.046
<b>Attainable Data Rate</b>	1.461	5.046
<b>Provisioned Data Rate</b>	1.461	5.046
<b>SNR Margin</b>	0.097	0.062
<b>Signal-level</b>	0	0
<b>Line Attenuation</b>	0.3	0.158
<b>BIN allocation</b>	0	0
<b>FEC Size</b>	0	0
<b>Codeword size</b>	0	0
<b>Interleave delay</b>	0.8	0.8
<b>CRC (Cyclic Redundancy Check) error count</b>	0.456	0.456
<b>HEC (Header Error Correction) error count</b>	2.873	2.873
<b>FEC (Forward Error Correction) error count</b>	156.473	156.473